

668220" 26T29E60

09362192.072899
668270 26129E60

FIG. 2A

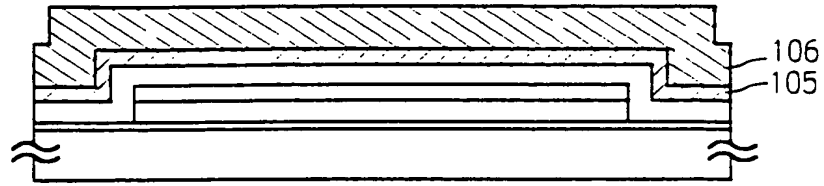


FIG. 2B

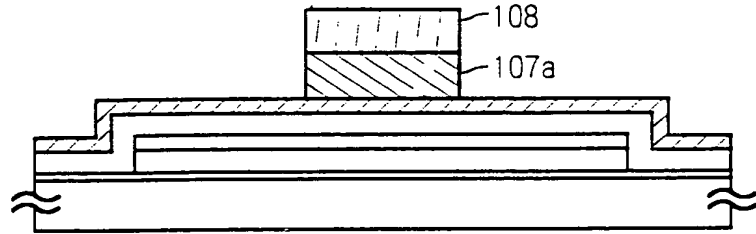


FIG. 2C

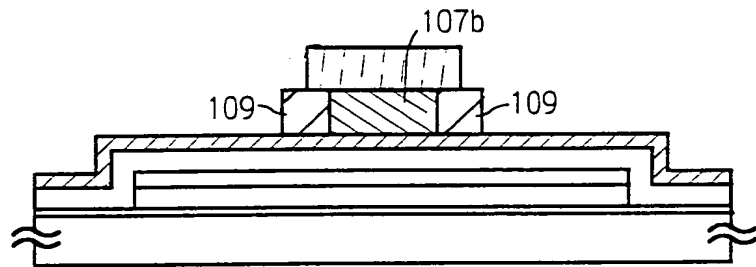


FIG. 2D

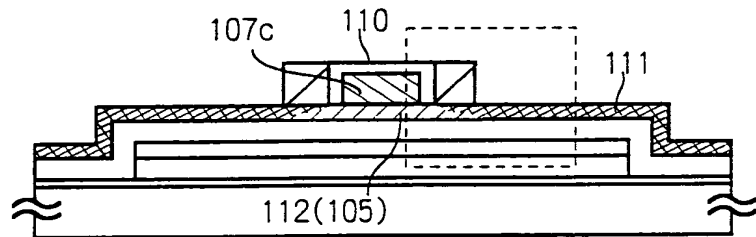


FIG. 2E

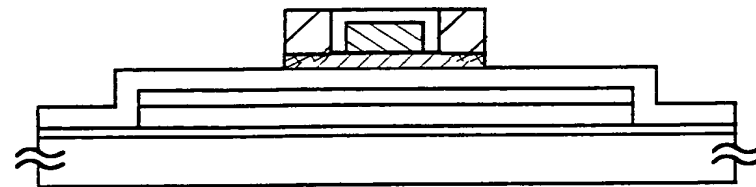


FIG. 3A

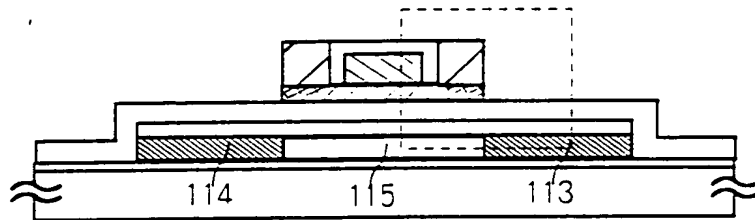


FIG. 3B

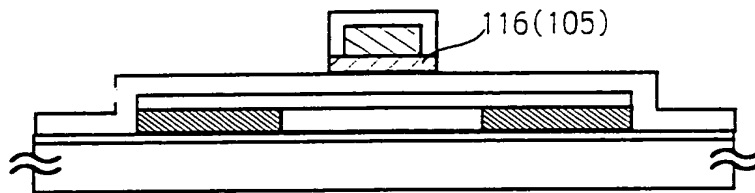


FIG. 3C

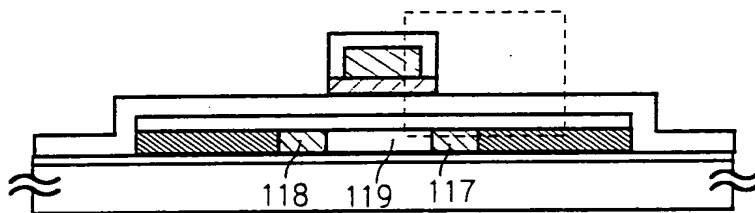


FIG. 3D

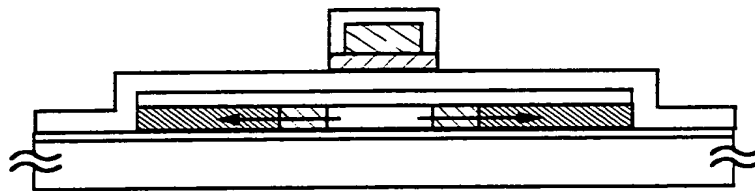


FIG. 3E

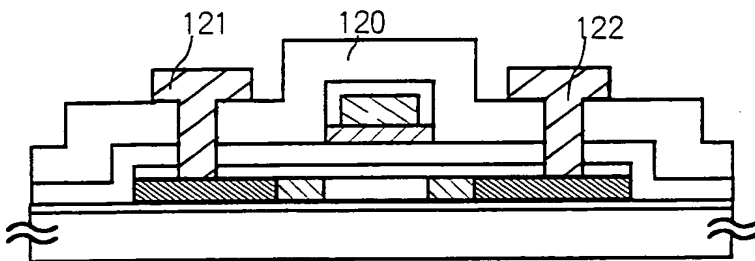


FIG. 4A

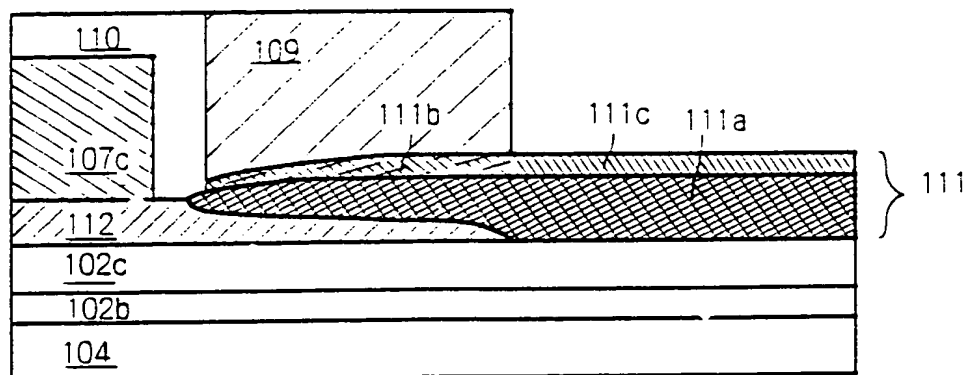


FIG. 4B

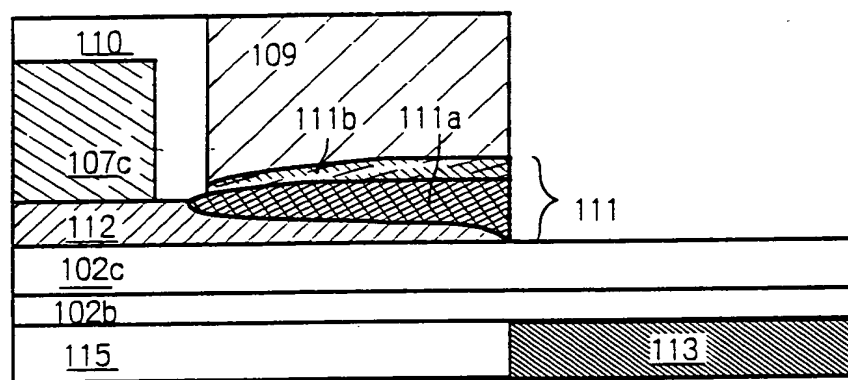
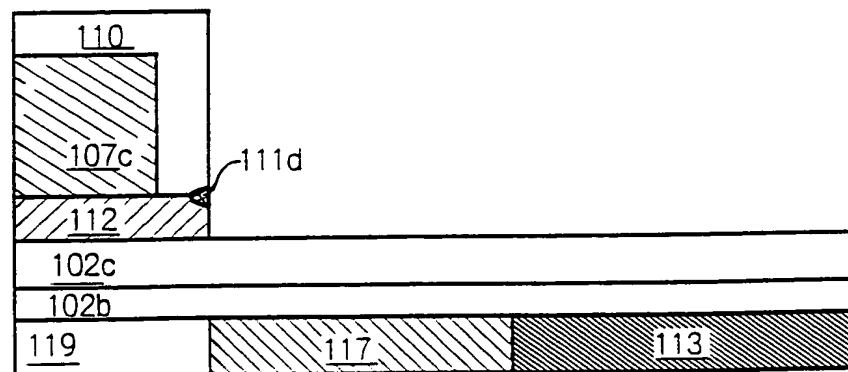


FIG. 4C



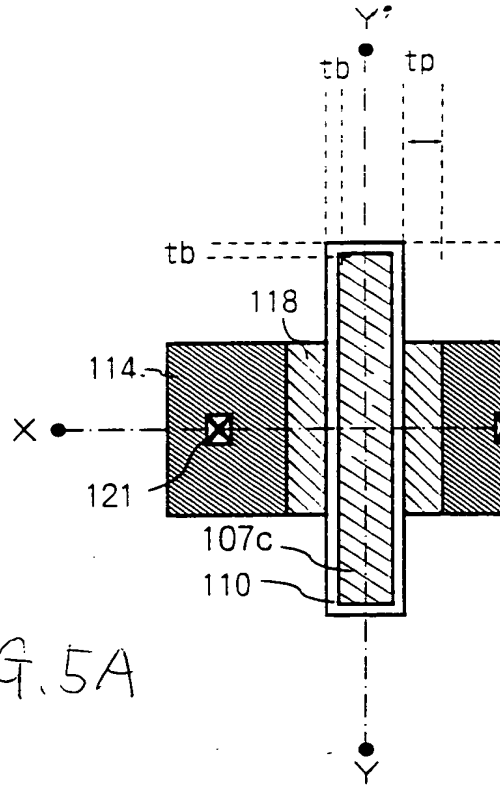


FIG. 5A

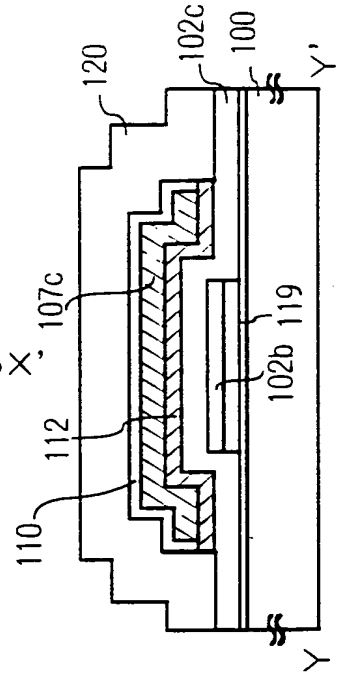


FIG. 5C

Y-Y' SECTION

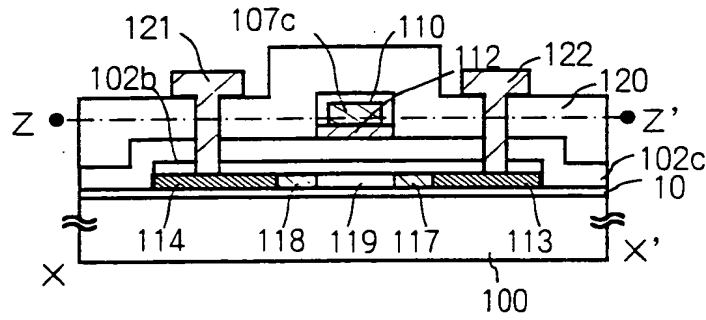
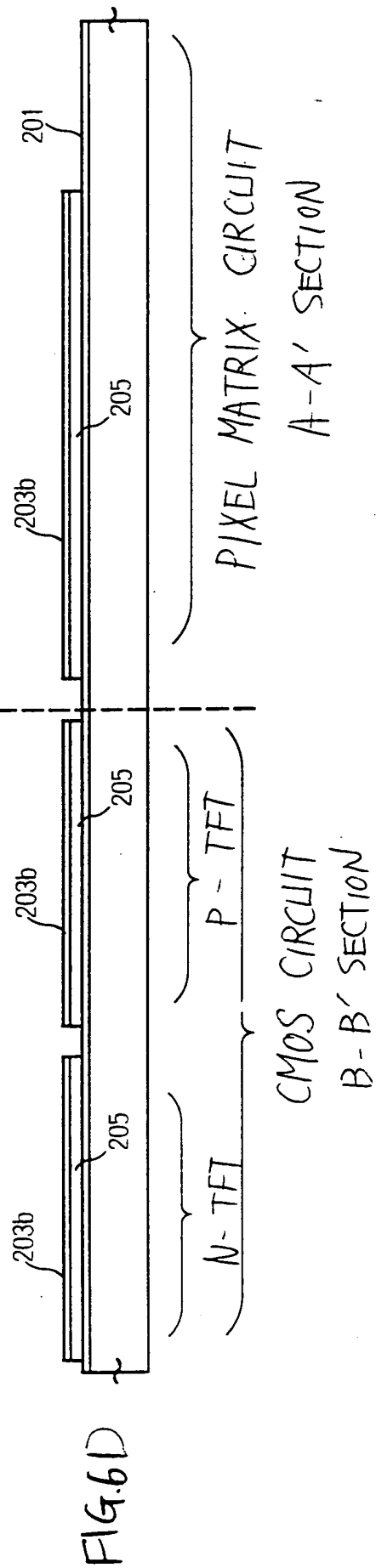
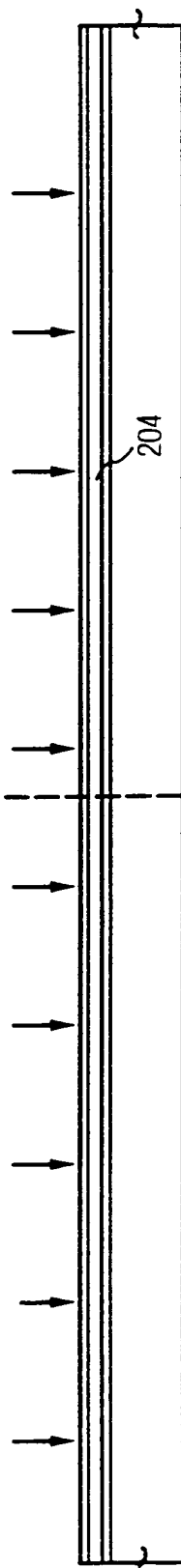
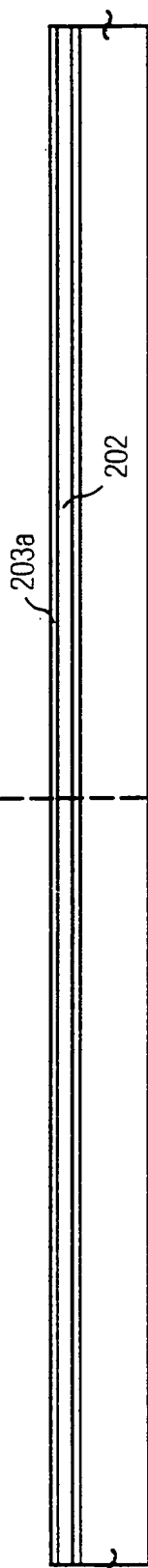
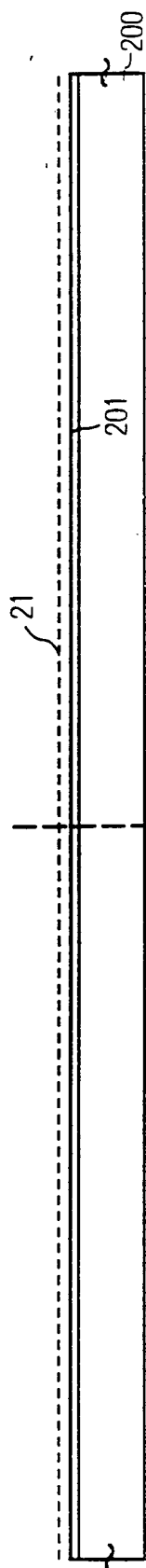
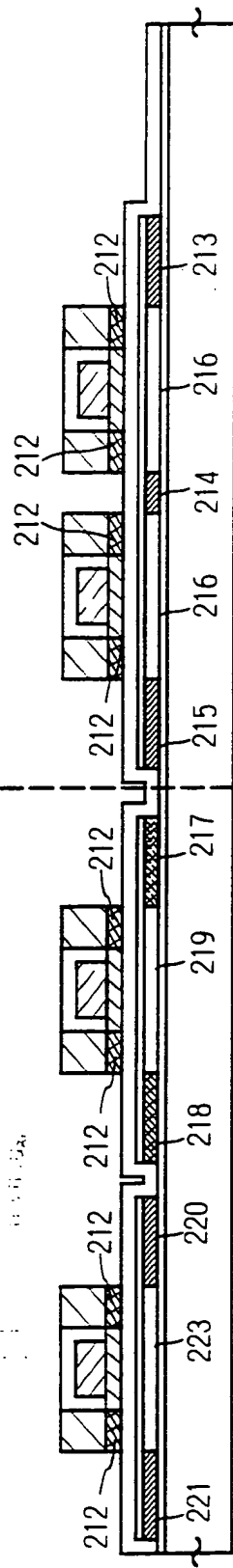
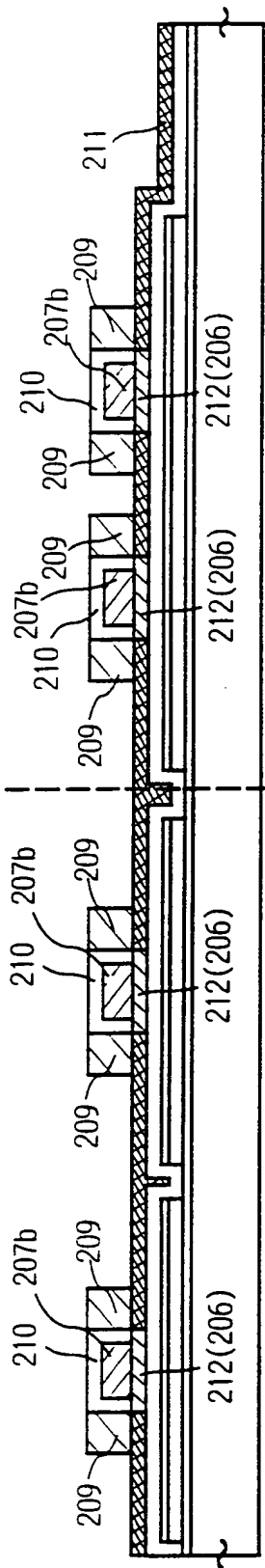
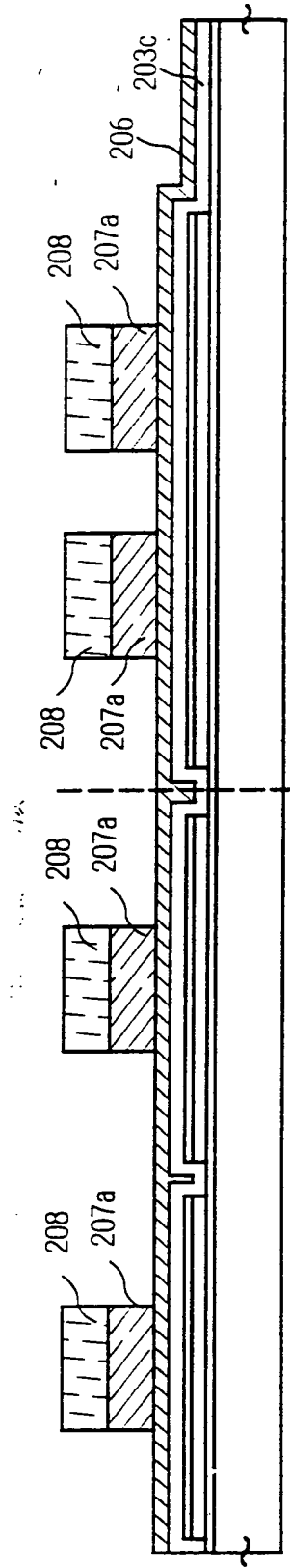


FIG. 5B

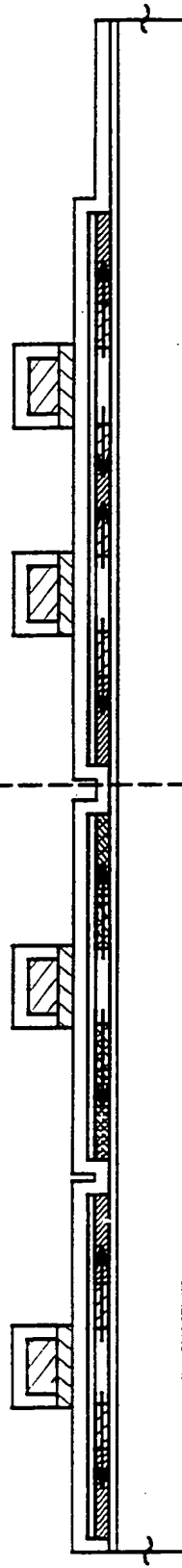
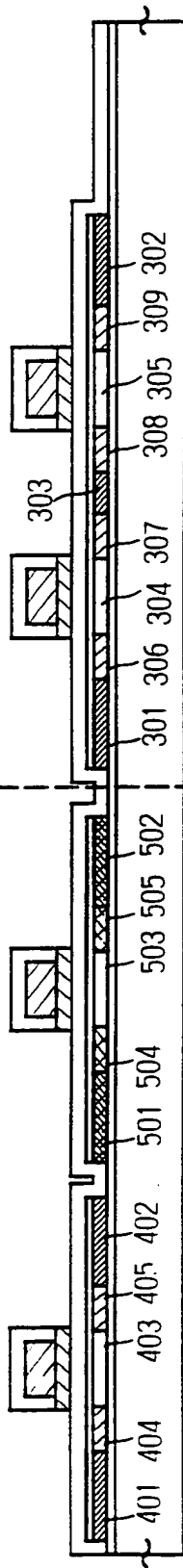
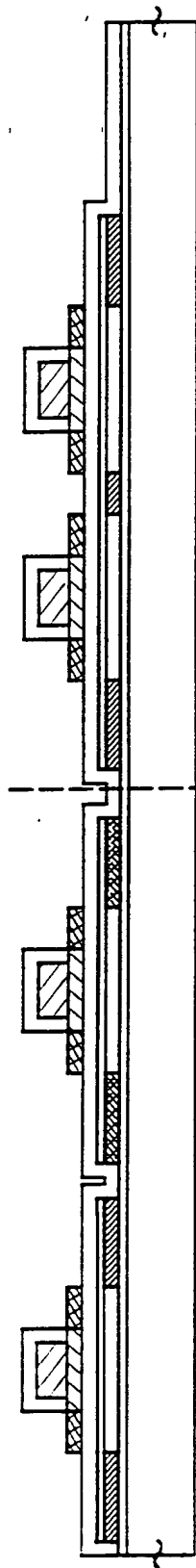
X-X' SECTION





PIXEL MATRIX CIRCUIT
A-A' SECTION

N-TFT
CMOS CIRCUIT
B-B' SECTION



N-TFT
 CMOS CIRCUIT
 B-B' SECTION
 P-TFT
 PIXEL MATRIX CIRCUIT
 A-A' SECTION

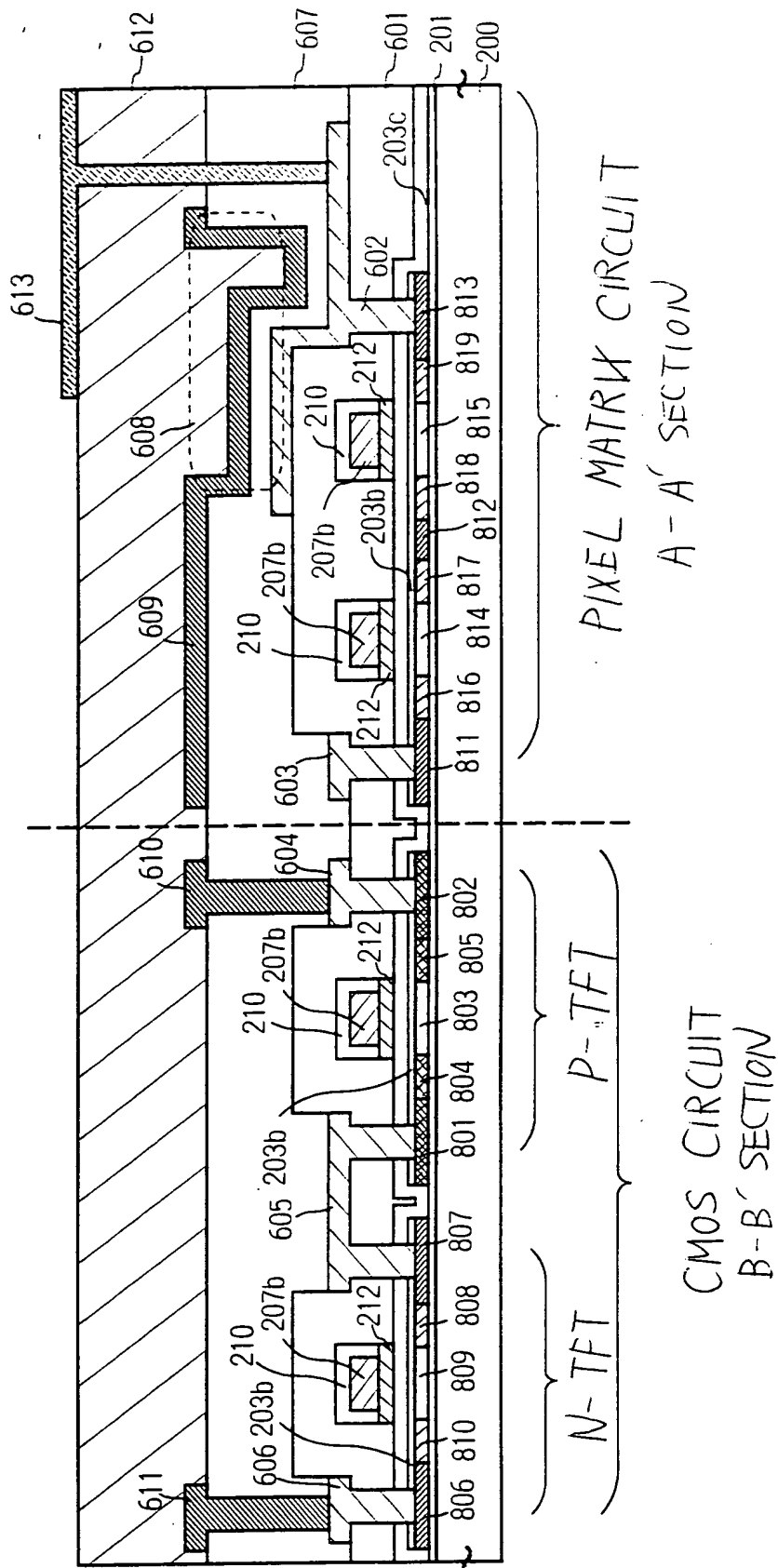


FIG. 9

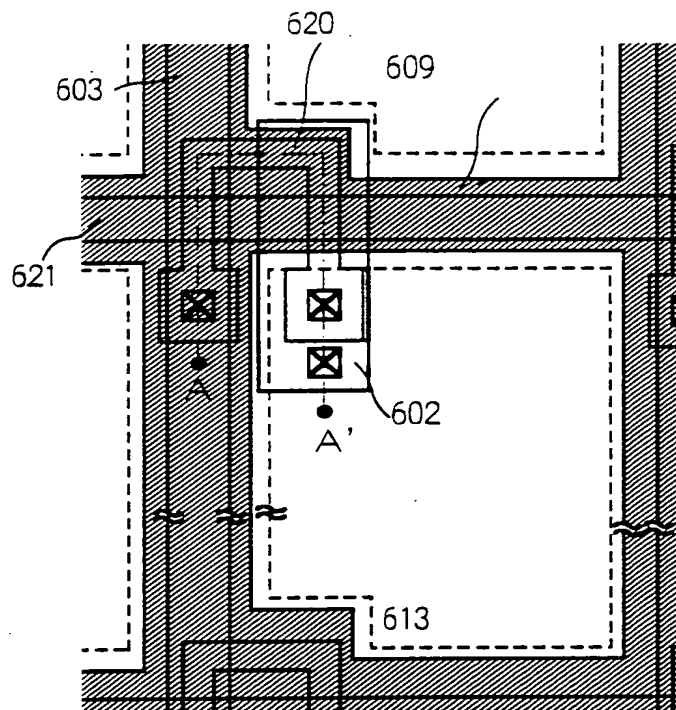


FIG. 10A

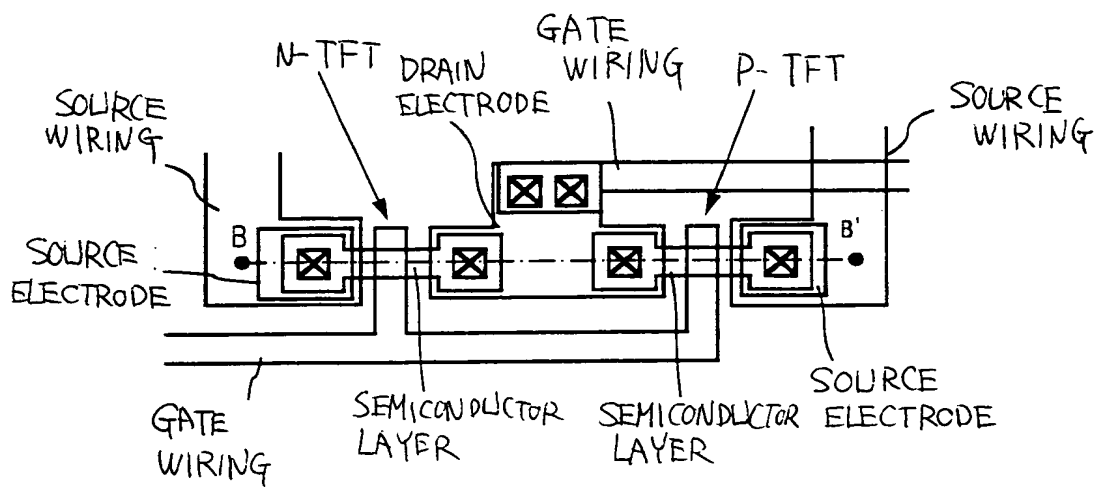


FIG. 10B

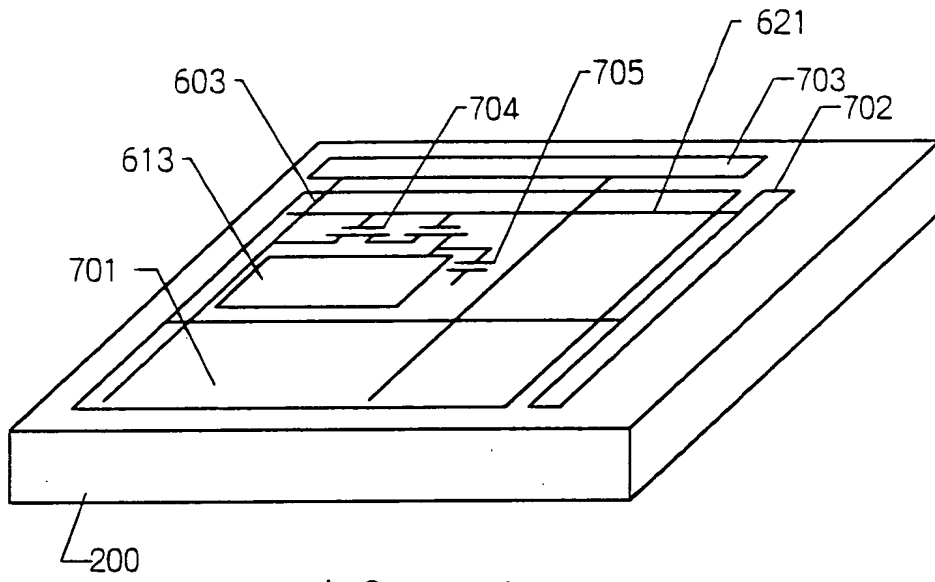


FIG. 11

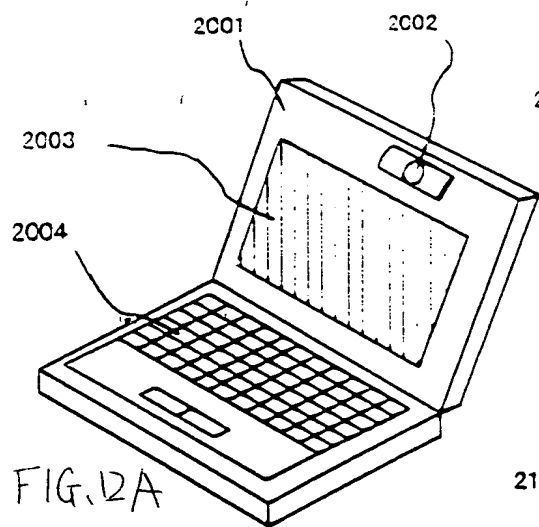


FIG. 12A

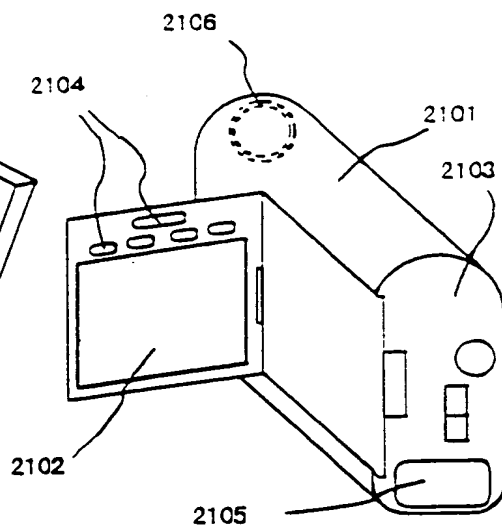


FIG. 12B

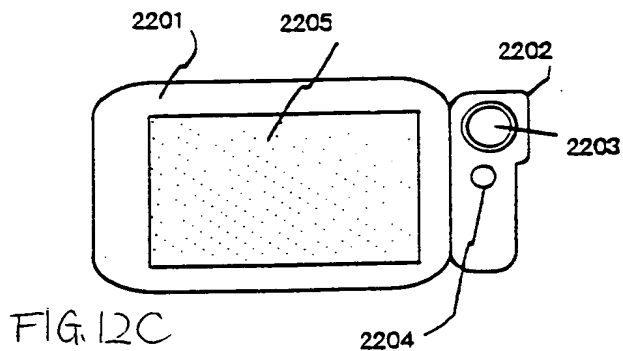


FIG. 12C

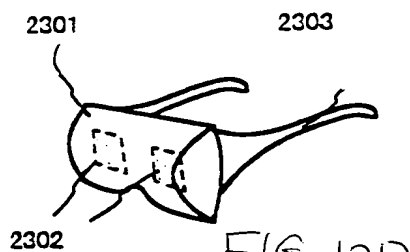


FIG. 12D

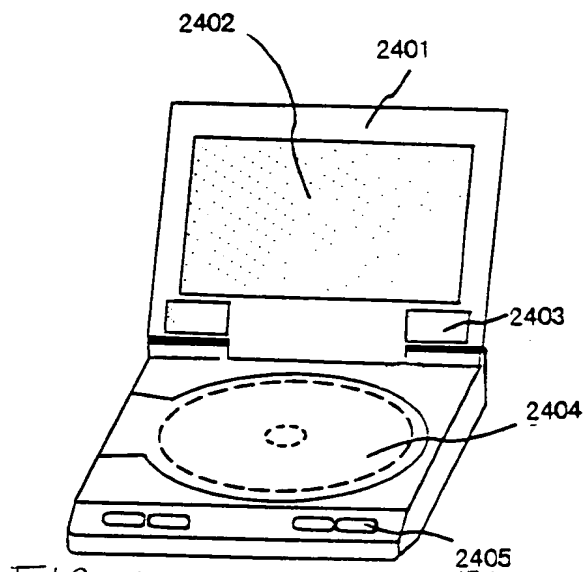


FIG. 12E

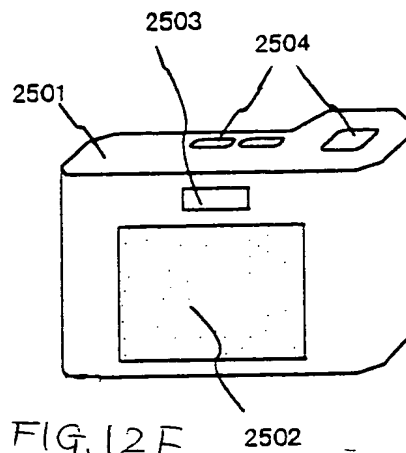


FIG. 12F

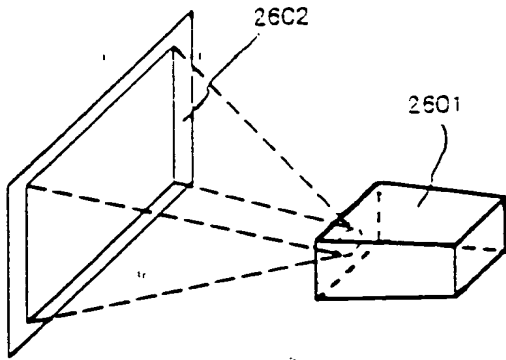


FIG. 13A

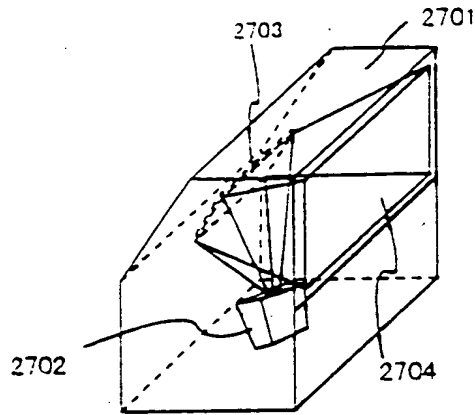


FIG. 13B

SCREEN

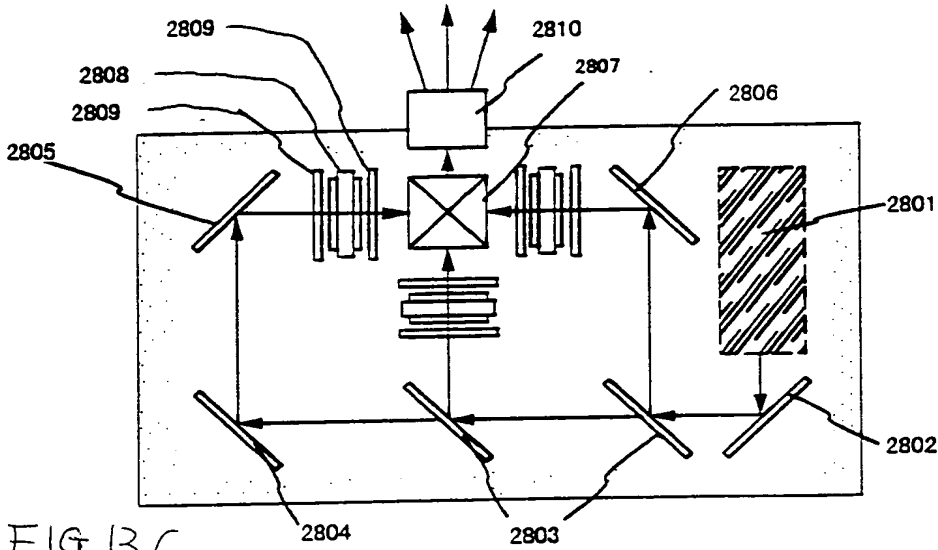


FIG. 13C

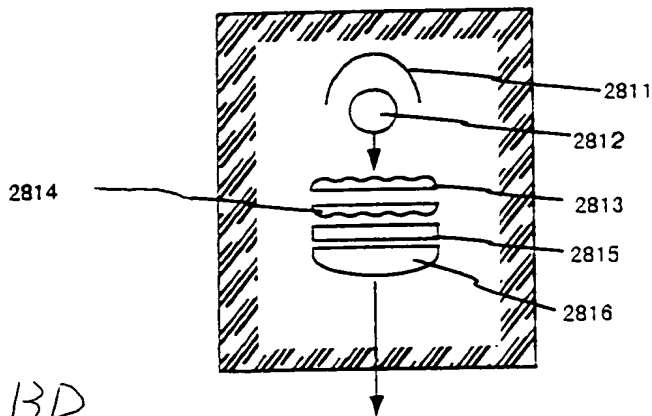


FIG. 13D

09362193.0729E60